

ARM® Cortex®-M0
32-bit Microcontroller

NuMicro® Family
NUC2201 Series BSP
Revision History

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Revision 3.00.004 (Released 2024-05-02)

1. Add timeout handler for infinite loop.
2. Use "volatile" with the function pointer to disable compiler optimizations in I2C sample code.
3. Modify UART_RS485_Master/Slave sample code.
4. Enable LVR in SYS_PowerDown_MinCurrent sample code to prevent power.
5. Update USB_D HID keyboard sample code to support LED status.
6. Modify VendorName of Nu_DFU.inf
7. Add SYS_PowerDown_MinCurrent sample code.
8. Call UART FIFO size from uart.h in USB_D VCOM sample code.
9. Fix USB_D MassStorage_CDROM crash on Linux.
10. Add I2C hang up & recover mechanism for I2C Master and Slave sample code.
11. Fix UART TX FIFO control issue in USB_D sample code.

Revision 3.00.003 (Released 2021-01-22)

1. Added to pass USB-IF CV-Chapter 9 & Class test of all USB_D Sample code.
2. Fixed warnings of adc driver in Library/StdDriver/src/adc.c.
3. Added SPI_TRIGGER_TX_RX_PDMA API.
4. Added Apache-2.0 license declaration in driver source.
5. Added README.md file.

Revision 3.00.002 (Released 2019-11-12)

1. Added ISP Sample codes to bsp\SampleCode\ISP folder.
2. Supports GNU GCC.
3. Added Mass Storage sample code to support SD Card.
4. Fixed PWM_DisableCaptureInt of PWM driver.

Revision 3.00.001 (Released 2018-07-06)

1. Initial Release.

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